深圳市希恩凯电子有限公司
SHENZHEN CNK ELECTRONIC CO.,LTD.

APPROVAL SHEET
承 认 书

Customer 客户名称	
Part NO. 产品型号	CNKT0430-14001C8
Product type 产品内容	Mode: Transmissive type .Normally white. TFT LCD Module
Remarks 备注栏	□APPROVAL FOR SEPCIFICATIONS ONLY ■APPROVAL FOR SEPCIFICATIONS AND SAMPLE
Signature by Customer: 客户确认签章	

Issued by 制作	Checked by 审查	Approved by 核准

1. General Specifications

No.	Item	Specification	Remark
1	LCD size	4.3 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	480 × 3 (RGB) × 272	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.066(W) × 0.198(H) mm	
6	Active area	95.04(W) × 53.856(H) mm	
7	Module size	105.4(W) × 67.1(H) ×4.1(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight Power consumption	TBD W(Typ.)	
12	Panel Power consumption	TBD W (Typ.)	
13	TFT Panel Supplier	TBD	

Note 1: Refer to Mechanical Drawing.

2. Pin Assignment

2.1.TFT LCD Panel Driving Section

FPC Connector is used for the module electronics interface. The recommended model is “FC19SC-40S-0.5SH” manufactured by HIROSE.

Pin No.	Symbol	I/O	Function	Remark
1	V_{LED-}	P	Power for LED backlight cathode	
2	V_{LED+}	P	Power for LED backlight anode	
3	GND	P	Power ground	
4	V_{DD}	P	Power voltage	
5	R0	I	Red data (LSB)	
6	R1	I	Red data	
7	R2	I	Red data	
8	R3	I	Red data	
9	R4	I	Red data	
10	R5	I	Red data	
11	R6	I	Red data	
12	R7	I	Red data (MSB)	
13	G0	I	Green data (LSB)	
14	G1	I	Green data	
15	G2	I	Green data	
16	G3	I	Green data	
17	G4	I	Green data	
18	G5	I	Green data	
19	G6	I	Green data	
20	G7	I	Green data (MSB)	

21	B0	I	Blue data (LSB)	
22	B1	I	Blue data	
23	B2	I	Blue data	
24	B3	I	Blue data	
25	B4	I	Blue data	
26	B5	I	Blue data	
27	B6	I	Blue data	
28	B7	I	Blue data (MSB)	
29	GND	P	Power ground	
30	CLK	I	Pixel clock	
31	DISP	I	Display on/off	
32	NC	-	No connection	
33	NC	-	No connection	
34	DE	I	Data Enable	
35	NC	-	No connection	
36	GND	P	Power ground	
37	XR	-	XR	
38	YD	-	YD	
39	YL	-	YL	
40	YU	-	YU	

I: input, O: output, P: Power

3. Operation Specifications

3.1. Absolute Maximum Ratings

(Note 1)

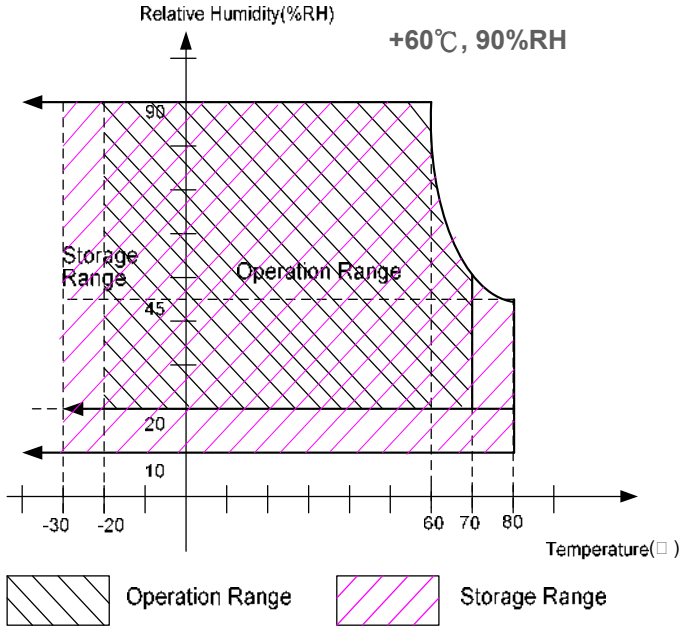
Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	V_{DD}	-0.5	5.0	V	
Input signal voltage	Logic input	-0.5	5.0	V	
Operation temperature	T_{OP}	-10	60	℃	Note 3, 4
Storage temperature	T_{ST}	-20	70	℃	Note 3, 4
LED Reverse Voltage	V_R	-	1.2	V	Each LED Note 2
LED Forward Current	I_F	-	25	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed.

Note 2: V_R Conditions: Zener Diode 20mA

Note 3: 90% RH Max. (Max wet temp. is 60℃)

Maximum wet-bulb temperature is at 60℃ or less. And No condensation (no drops of dew)



Note 4: In case of temperature below 0℃,the response time of liquid crystal (LC) becomes slower and the color of panel darker than normal one.

3.2. Typical operation conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	V_{DD}	3.1	3.3	3.5	V	
Current for Driver	IV_{DD}	-	TBD	25	mA	V_{DD} = 3.3V
Input logic high voltage	V_{IH}	$0.8V_{DD}$	-	V_{DD}	V	Note 1
Input logic low voltage	V_{IL}	GND	-	$0.2V_{DD}$	V	

Note1: CLK, DE, R0~ R7, G0~ G7, B0~ B7.

3.3 Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED Backlight	V_L	21	21.7	23.1		Note 2
Current for LED Backlight	I_L	18	20	22	mA	
LED life time	-	20,000	-	-	Hr	Note 1

Note 1: The “LED life time” is defined as the module brightness decrease to 50% original brightness that the ambient temperature is 25℃ and I_L =20mA. The LED lifetime could be decreased if operating I_L is lager than 20 mA.

Note 2: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and I_L =20mA.

3.5. Timing Characteristics

3.5.1. Timing Conditions

Parallel DE mode RGB input timing table

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
CLK frequency	fclk	7	9	12	MHz
DEV period time	Tv	277	288	400	H
DEV display area	Tvd		272		H
DEV blanking	Tvb	5	16	128	H
DEH period time	Th	520	525	800	CLK
DEH display area	Thd		480		CLK
DEH blanking	Thb	40	45	320	CLK
CLK cycle time	Tclk	83	110	143	ns
Clock width of high level	Tcwh	40	50	60	%
Clock width of low level	Tcwl	40	50	60	%
Clock rising time	t_{rck}		-	9	ns
Clock falling time	t_{fck}		-	9	ns
Data Setup Time	t_{desu}	10	-	-	ns
Data Hold Time	t_{dahd}	10	-	-	ns
DE Setup Time	t_{desu}	10	-	-	ns
DE Hold Time	t_{dehd}	10	-	-	ns

4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥ 10)	θ_L	Φ=180°(9 o'clock)	60	70	-	degree	Note 1
	θ_R	Φ=0°(3 o'clock)	60	70	-		
	θ_T	Φ=90°(12 o'clock)	60	70	-		
	θ_B	Φ=270°(6 o'clock)	40	50	-		
Response time	T_{ON}	Normal θ=Φ=0°	-	10	20	msec	Note 3
	T_{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR		TBD		-	-	Note 4
Color chromaticity	W_X		0.27	0.31	0.32	-	Note 2 Note 5 Note 6
	W_Y		0.27	0.31	0.32	-	
Luminance	L			500	-	cd/m²	Note 6
Luminance uniformity	Y_U		75		-	%	Note 7

Test Conditions:

1. V_{DD}=3.3V, I_L=20mA (Backlight current), the ambient temperature is 25℃.
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

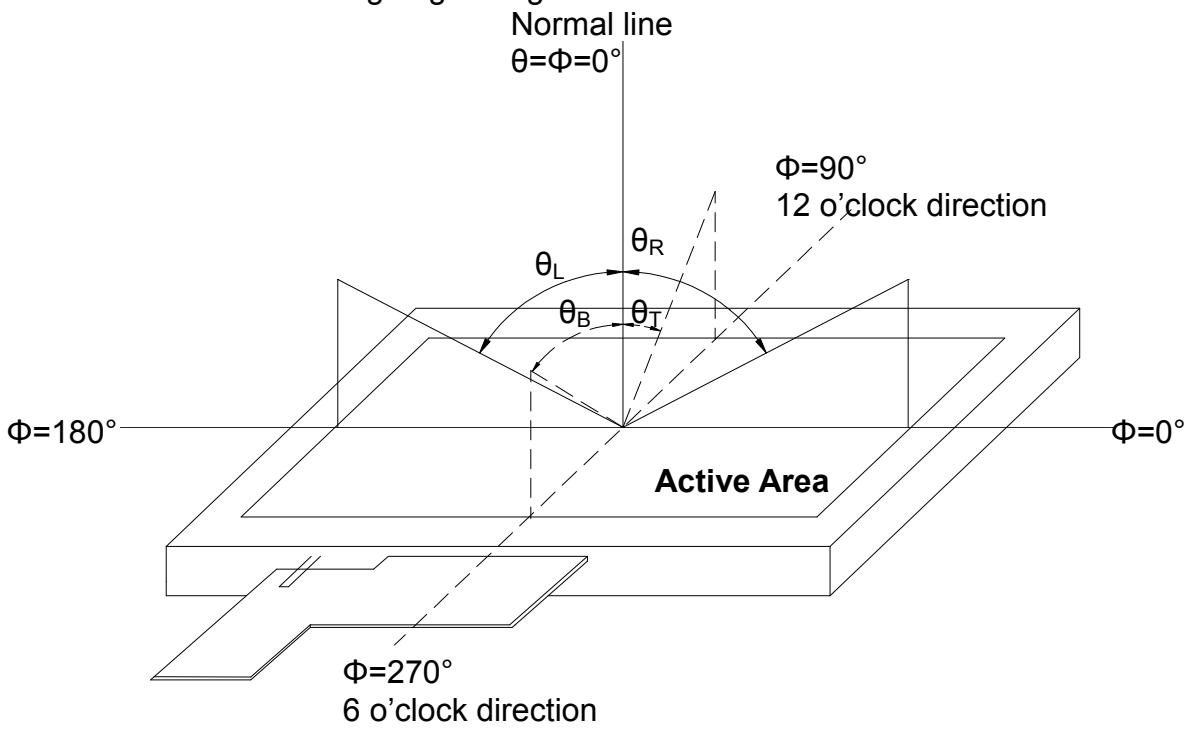


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

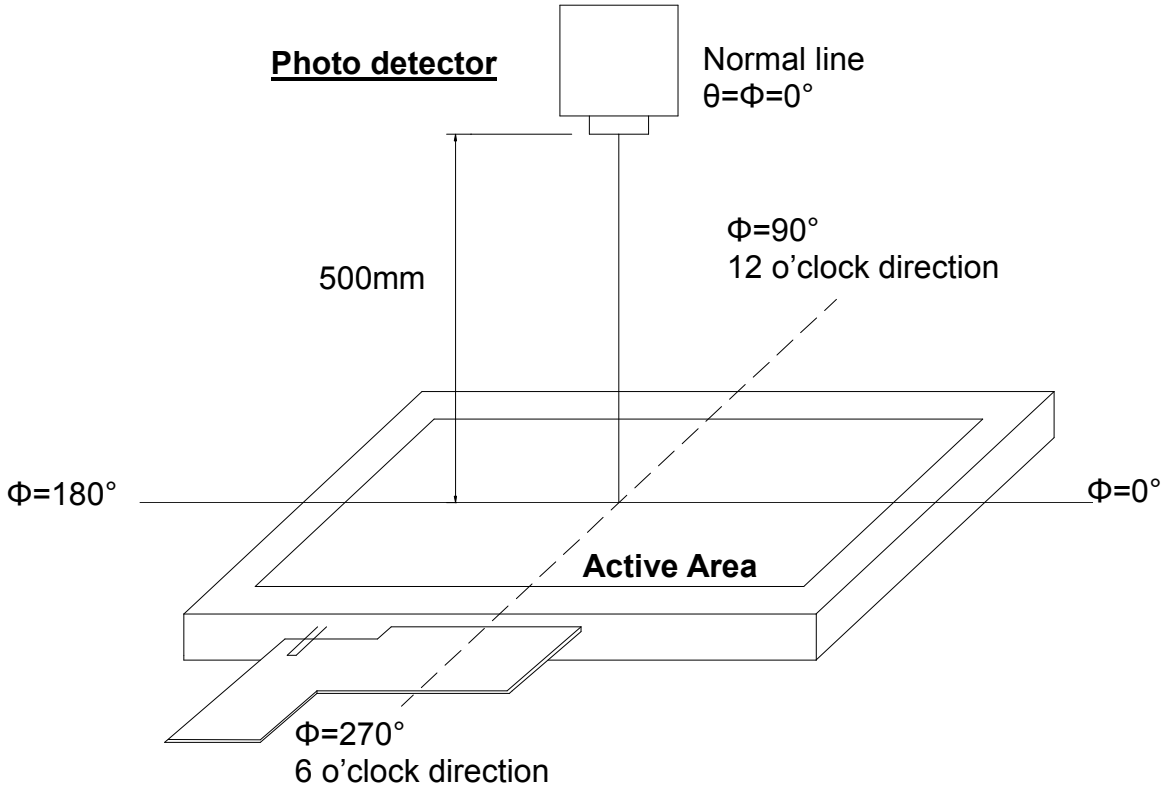


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

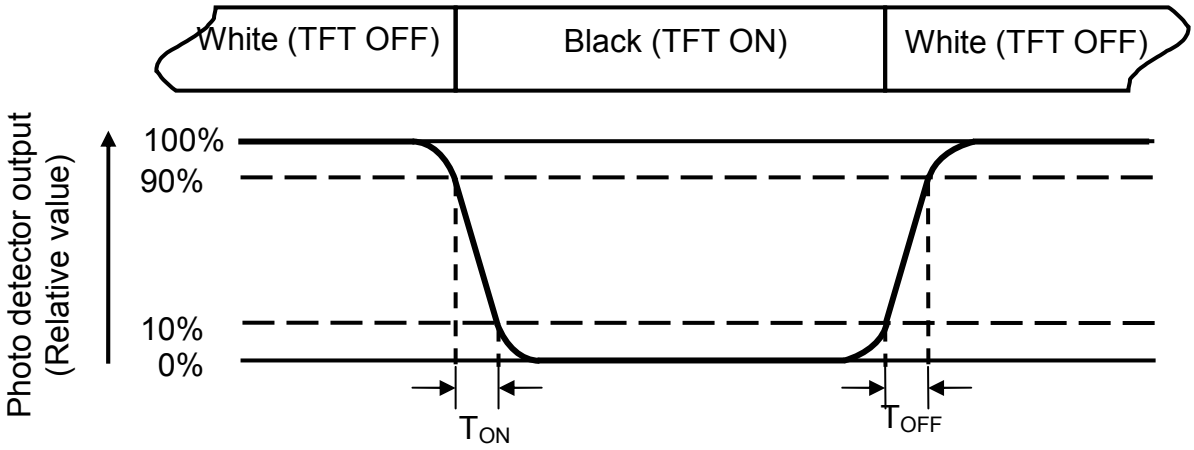


Fig. 4-3 Definition of response time while measuring the center area of

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)
Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground
the panel. The LED driving condition is I_L=20mA.

Note 7:

Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$Luminance\ \ Uniformity\ (Yu) = \frac{B_{min}}{B_{max}}$$

L-------Active area length W----- Active area width

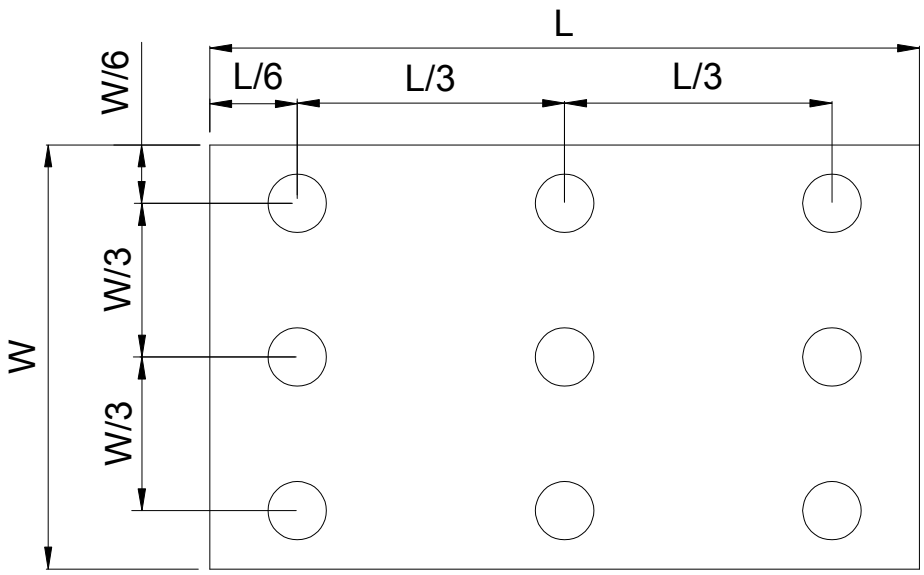


Fig. 4-4 Definition of measuring points

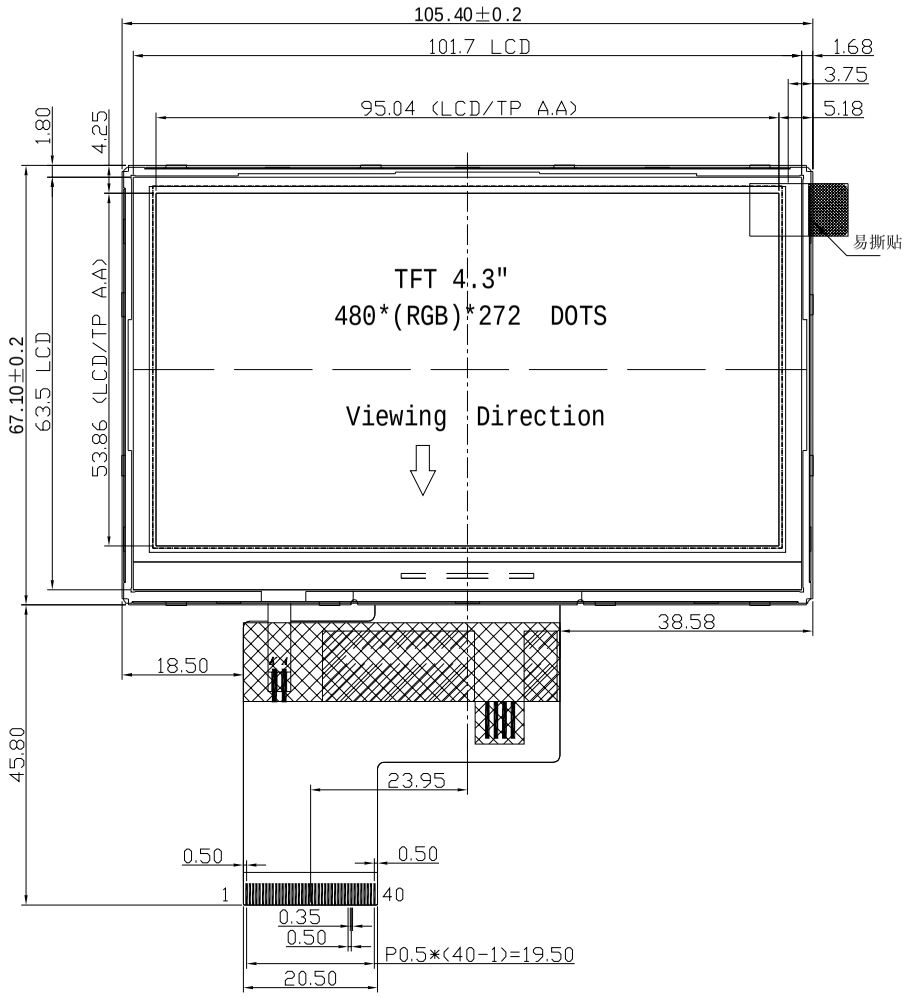
B_{max}: The measured maximum luminance of all measurement position.
B_{min}: The measured minimum luminance of all measurement position.

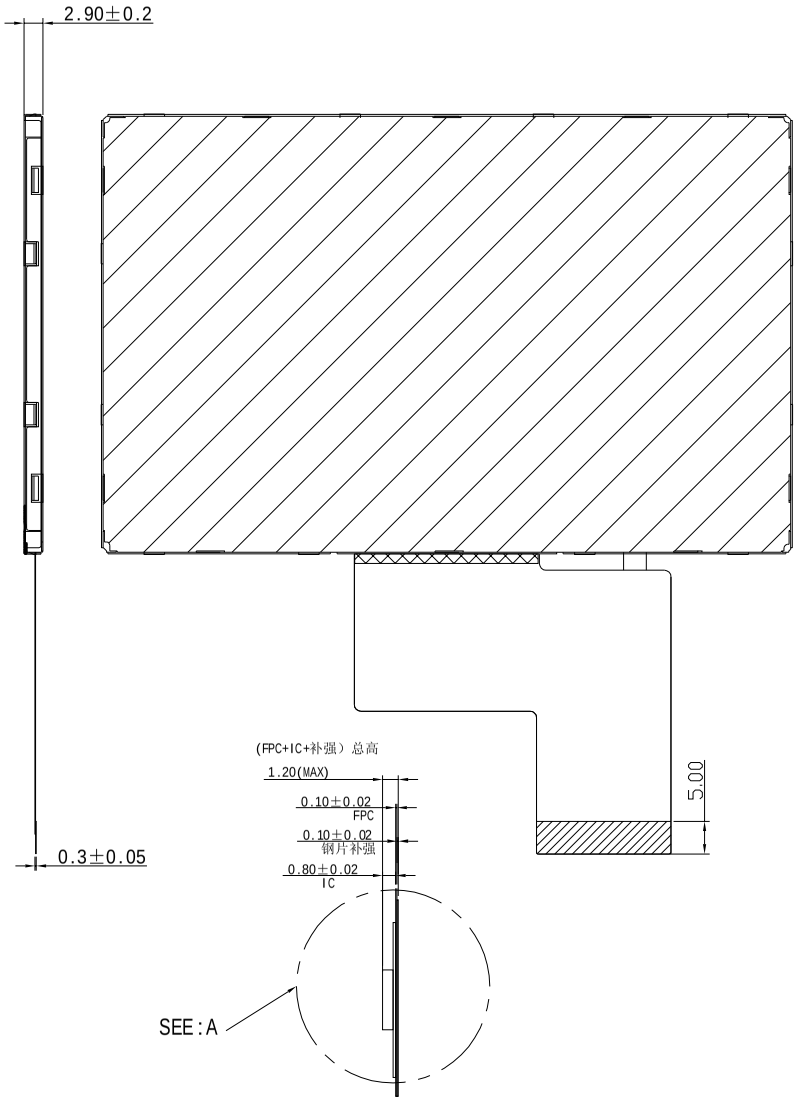
5. Reliability Test Items

(Note3)

Item	Test Conditions	Remark
High Temperature Storage	Ta = 70℃ 240 hrs	Note 1,Note 4
Low Temperature Storage	Ta = -20℃ 240hrs	Note 1,Note 4
High Temperature Operation	Ts = 60℃ 240hrs	Note 2,Note 4
Low Temperature Operation	Ta = -10℃ 240hrs	Note 1,Note 4
Operate at High Temperature and Humidity	+40℃, 90%RH 240 hrs	Note 5
Thermal Shock	-10℃/30 min ~ +70℃/30 min for a total 100 cycles, Start with cold temperature and end with high temperature	Note 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package Drop Test	Height:60 cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.
Note 2: Ts is the temperature of panel's surface.
Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.
Note 4: Before cosmetic and function tests, the product must have enough recovery time, at least 2 hours at room temperature.





LCM

No	PIN NAME
1	LEDK
2	LEDA
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	CLK
31	DISP
32	NC
33	NC
34	DE
35	NC
36	GND
37	NC
38	NC
39	NC
40	YU

LED CIRCUIT DIAGRAM:

1. DISPLAY TYPE	TFT/NEGATIVE	Approve		Unmarked Tolerance	±0.2
2. VIEWING DIRECTION	12 O'CLOCK				
3. DRIVER IC	ST7282	Checked		Unit	mm
4. LED VOLTAGE	26.4V(15mA)			Version	A.0
5. OPERATIVE VOLTAGE	3.3V	Drawn		Date	2014

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